

## ETHERNET IN CIRCUITRY TO DATA PROCESSOR\_

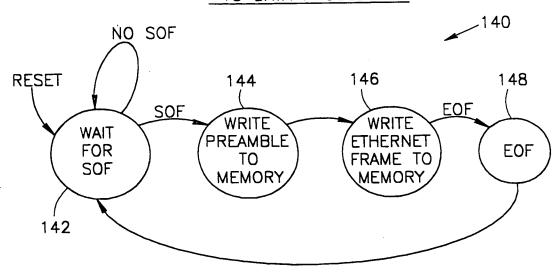
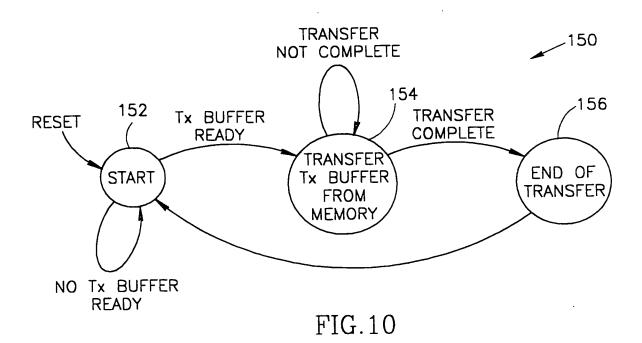
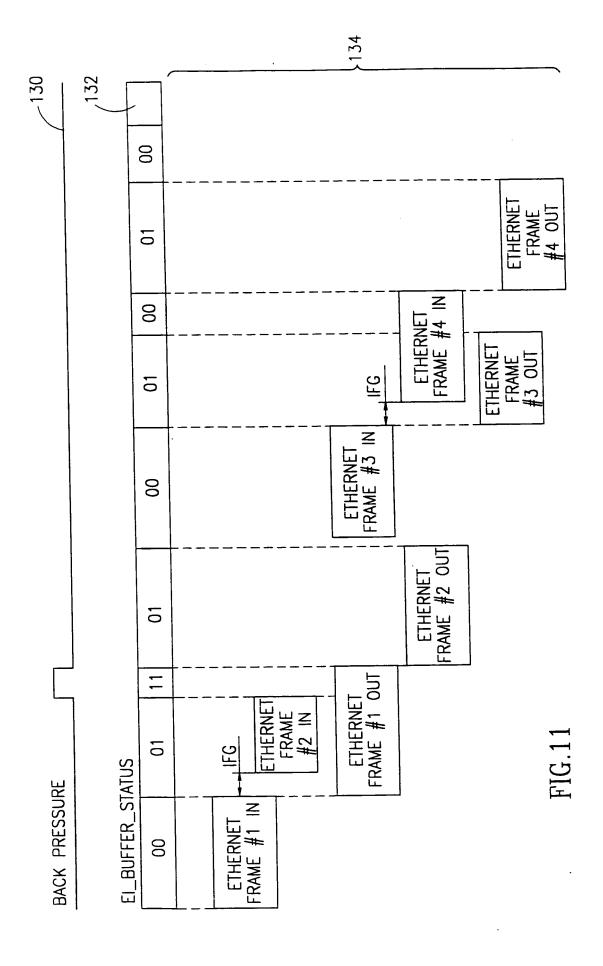
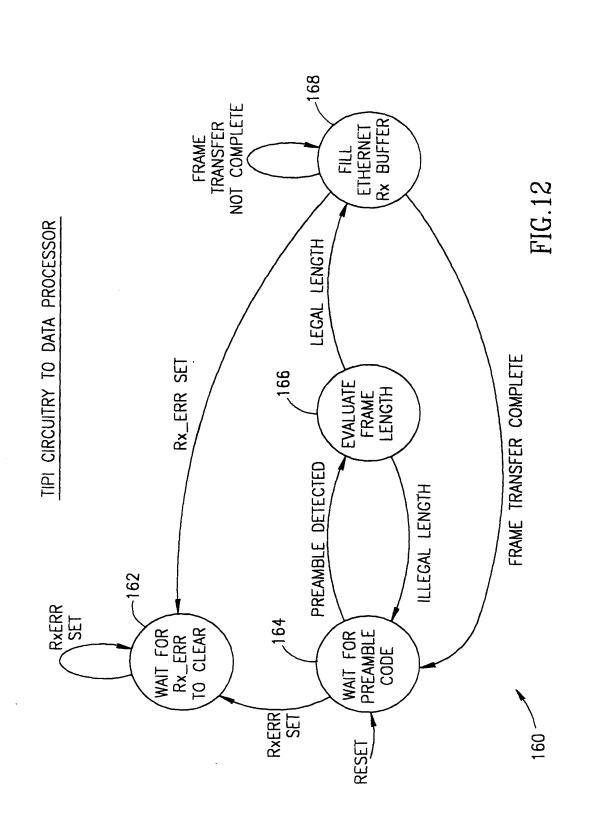


FIG.9

## DATA PROCESSOR TO TIPO CIRCUITRY







END OF TRANSFER TRANSFER COMPLETE TRANSFER NOT COMPLETE TRANSFER CONTENTS OF Rx BUFFER DATA PROCESSOR
TO ETHERNET OUT CIRCUITRY IFG DELAY FINISHED 176 READ LENGTH OF FRAME Rx BUFFER READY NO Rx BUFFER READY WAIT FOR Rx BUFFER READY 172-RESET

FIG.13